

**WHAT IS CLAIMED IS:**

1. A set of Universal Storage Interface Bus device, comprising  
an IDE storage device, providing a first interface;  
a ribbon cable, providing a first connector coupling with the IDE storage  
5 device and providing a second connector;  
an USIB connector conversion board, providing a third connector, and a  
fourth connector, the third connector coupling with said second connector,  
converts the IDE signal and connector of the IDE data storage device into  
USIB signal coming out of the fourth connector, a standard MC-36 connector;  
10 and  
an USIB interface conversion cable, providing a fifth connector, an USIB  
interface conversion board and a sixth connector, a cable being between and  
connecting with the USIB interface conversion board and the sixth connector,  
the fifth connector coupling with said fourth connector, and the sixth connector  
15 coupling with an input/out port of a personal computer;  
whereby, the data storage device can be connected to the input/output  
port with any type of interface by way of replacing with the appropriate USIB  
interface conversion cable only.
2. The Universal Storage Interface Bus device according to claim 1, wherein the  
20 USIB interface conversion board further comprises an ASIC to convert a signal  
of the first interface into a signal of a second interface.
3. The Universal Storage Interface Bus device according to claim 1, wherein the  
first interface is IDE interface.
4. The Universal Storage Interface Bus according to claim 2, wherein the second  
25 interface is a UBS interface.
5. The Universal Storage Interface Bus device according to claim 2, wherein the  
second interface is a PCMCIA interface.

6. The Universal Storage Interface Bus device according to claim 2, wherein the second interface is a Firewire interface.
7. The Universal Storage Interface Bus device according to claim 2, wherein the second interface is any future input/output interface.
- 5 8. The Universal Storage Interface Bus device according to claim 1, wherein the first, the second, and the third connectors are a 50-pin connector respectively.
9. The Universal Storage Interface Bus device according to claim 1, wherein the fourth and the fifth connectors are a MC-36 connector respectively.
- 10 10. The Universal Storage Interface Bus device according to claim 9, wherein the respective MC-36 connector has 36 pins be defined as follows respectively: a 1st pin is RESRET, a 2nd pin is D7, a 3rd pin is D6, a 4th pin is D5, a 5th pin is D4, a 6th pin is D3, a 7th pin is D2, a 8th pin is D1, a 9th pin is D0, a 10th pin is +5V, Device, 11th pin is 10W, 12th pin is IOR, a 13th pin is ground, a 14th pin is DMAACK, a 15th pin is IRQ, a 16th pin is A1, a 17th pin is A0, a 18th pin is CS0, a 19th pin is GND, a 20th pin is D8, a 21st pin is D9, a 22nd pin is D10, a 23rd pin is D11, a 24th pin is D12, a 25th pin is D13, a 26th pin is D14, a 27th pin is D15, a 28th pin is Ground, a 29th pin is Ground, a 30th pin is +12V, a 31st pin is \*IORDY, a 32nd pin is IORDY/IICS16, a 33rd pin is +5V, HOST, a 34th pin is A2, a 35th is a CS1, and a 36th pin is DMAREQ.

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